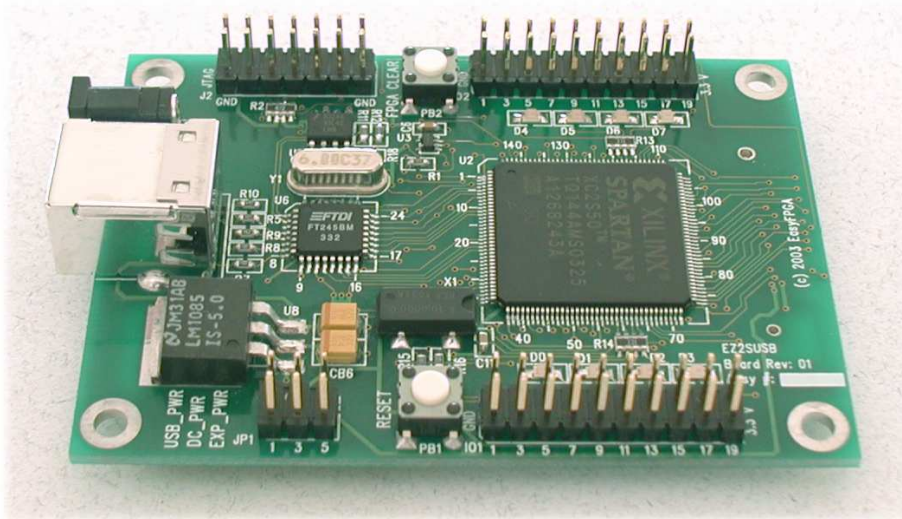


EZ2SUSB—Xilinx Spartan-II Development Board

FPGA Development board with USB interface



Features

- Connects to the host PC via USB
- USB data transfer up to 1MB per second
- Easy configuration via USB or JTAG
- High Density FPGA (15,000 to 50,000 gates, up to 24,576 RAM bits, 4 DLLs)
- On-board clock oscillator and reset circuitry
- 58 I/O pins (5-V tolerant)
- 2 dedicated clock/input pins
- Internal Clock Output/ External Clock Input
- External Reset Input
- Eight LEDs
- Reset Push-button, FPGA Clear Push-button
- Royalty free USB driver (Win. 98/Me/2000/XP)
- No specific USB firmware programming required
- VB6/VB.NET sample applications
- VHDL samples

Applications

- Rapid prototyping and development of Xilinx Spartan-II design
- IP development and testing
- Telecommunications
- Digital Signal Processing
- Data acquisition and control
- Signal generation
- USB Digital Camera Interface
- USB to synchronous bus bridge

Featured FPGAs

- EZ2SUSB-15: Xilinx Spartan-II XC2S15-5TQ144 15,000 system gates, 432 LCs, 6,144 RAM bits
- EZ2SUSB-30: Xilinx Spartan-II XC2S30-5TQ144 30,000 system gates, 972 LCs, 13,824 RAM bits
- EZ2SUSB-50: Xilinx Spartan-II XC2S50-5TQ144 50,000 system gates, 1,728 LCs, 24,576 RAM bits

System Specification

- Physical dimensions: 3.0" x 2.2"
- DC Input: 6V, 250 mA, 1.3 mm power jack
- 2x 20 pin 0.100" (2.54 mm) pitch header (top side)
- 1x high density 50 pin 0.8 mm pitch header (bottom side)
- USB (type "B") connector

Cables And Accessories

- DC Power Supply
- USB Drivers
- USB A->B cable
- VB6/VB.NET examples
- Documentation
- VHDL code examples

Warranty

- One year - parts, labor, technical support
- Free software upgrades

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