

EZ1CUSB Development Board Pinout

LEDs		
Signal Name	FPGA Pin #	Signal Type
LED0	135	Output
LED1	134	Output
LED2	133	Output
LED3	132	Output
LED4	128	Output
LED5	127	Output
LED6	126	Output
LED7	125	Output

JTAG		
Signal Name	FPGA Pin #	Signal Type
TDI	155	Input
TDO	149	Output
TMS	148	Input
TCK	147	Input

Internal Clocks and Reset		
Signal name	FPGA Pin #	Signal Type
INT_CLOCK_A	28	Clock/Input
INT_CLOCK_B	153	Clock/Input
RESET#	50	Input

Unused FPGA Pins	
Pin Number	122, 123, 124, 136, 137, 138

FT2232C Port B (USB FIFO Interface)		
Signal Name	FPGA Pin #	Signal Type
USB_D0	1	Bi-directional
USB_D1	2	Bi-directional
USB_D2	4	Bi-directional
USB_D3	5	Bi-directional
USB_D4	6	Bi-directional
USB_D5	7	Bi-directional
USB_D6	8	Bi-directional
USB_D7	12	Bi-directional
USB_RXF#	13	Input
USB_TXE#	14	Input
USB_RD#	15	Output
USB_WR	16	Output

FT2232C Port A (FPGA/EEPROM Programming Interface)		
Signal Name	FT2232C Pin #	Signal Type
DCLK	24	Output
DATA0	23	Output
CONFIG#	22	Output
CFG_DONE#	21	Input
CE#	20	Output
CS#	19	Output
ASDO	17	Bi-directional
PS/AS#	16	Output

J3 (Bottom side)			
Header Pin #	Signal Name	FPGA Pin #	Signal Type
1	3V3	-	Power/Output
2	VCC	-	Power/Output
3	IO_BOT1	76	Bi-directional
4	IO_BOT0	75	Bi-directional
5	IO_BOT3	78	Bi-directional
6	IO_BOT2	77	Bi-directional
7	IO_BOT5	82	Bi-directional
8	IO_BOT4	79	Bi-directional
9	IO_BOT7	84	Bi-directional
10	IO_BOT6	83	Bi-directional
11	IO_BOT9	86	Bi-directional
12	IO_BOT8	85	Bi-directional
13	EXT_RESET_IN#	-	Reset/Input
14	GND	-	Power
15	IO_BOT11	88	Bi-directional
16	IO_BOT10	87	Bi-directional
17	IO_BOT13	94	Bi-directional
18	IO_BOT12	93	Bi-directional
19	IO_BOT15	98	Bi-directional
20	IO_BOT14	95	Bi-directional
21	IO_BOT17	100	Bi-directional
22	IO_BOT16	99	Bi-directional
23	IO_BOT19	104	Bi-directional
24	IO_BOT18	101	Bi-directional
25	IO_BOT21	106	Bi-directional
26	IO_BOT20	105	Bi-directional
27	INT_CLOCK_B_BOT	-	Clock/Output
28	GND	-	Power
29	IO_BOT23	113	Bi-directional
30	IO_BOT22	107	Bi-directional
31	IO_BOT25	115	Bi-directional
32	IO_BOT24	114	Bi-directional
33	IO_BOT27	117	Bi-directional
34	IO_BOT26	116	Bi-directional
35	IO_BOT29	119	Bi-directional
36	IO_BOT28	118	Bi-directional
37	IO_BOT31	121	Bi-directional
38	IO_BOT30	120	Bi-directional
39	GND	-	Power
40	VCC_IO_EXT	-	Power/Input

J4 (Bottom side)			
Header Pin #	Signal Name	FPGA Pin #	Signal Type
1	GND	-	Power
2	VCC_EXT	-	Power/Input
3	IO_BOT33	187	Bi-directional
4	IO_BOT32	188	Bi-directional
5	IO_BOT35	185	Bi-directional
6	IO_BOT34	186	Bi-directional
7	IO_BOT37	183	Bi-directional
8	IO_BOT36	184	Bi-directional
9	IO_BOT39	181	Bi-directional
10	IO_BOT38	182	Bi-directional
11	IO_BOT41	179	Bi-directional
12	IO_BOT40	180	Bi-directional
13	CLOCK_IN_BOT	-	Clock/Input
14	GND	-	Power
15	IO_BOT43	177	Bi-directional
16	IO_BOT42	178	Bi-directional
17	IO_BOT45	175	Bi-directional
18	IO_BOT44	176	Bi-directional
19	IO_BOT47	173	Bi-directional
20	IO_BOT46	174	Bi-directional
21	IO_BOT49	168	Bi-directional
22	IO_BOT48	169	Bi-directional
23	IO_BOT51	166	Bi-directional
24	IO_BOT50	167	Bi-directional
25	IO_BOT53	164	Bi-directional
26	IO_BOT52	165	Bi-directional
27	PLL1_OUT_BOT	-	Clock/Output
28	GND	-	Power
29	IO_BOT55	162	Bi-directional
30	IO_BOT54	163	Bi-directional
31	IO_BOT57	160	Bi-directional
32	IO_BOT56	161	Bi-directional
33	IO_BOT59	158	Bi-directional
34	IO_BOT58	159	Bi-directional
35	IO_BOT61	141	Bi-directional
36	IO_BOT60	156	Bi-directional
37	IO_BOT63	139	Bi-directional
38	IO_BOT62	140	Bi-directional
39	GND	-	Power
40	INT_CLOCK_A_BOT	-	Clock/Output

J1 (Top side)			
Header Pin #	Signal Name	FPGA Pin #	Signal Type
1	VCC	-	Power/Output
2	3V3	-	Power/Output
3	IO_TOP0	17	Bi-directional
4	IO_TOP1	18	Bi-directional
5	IO_TOP2	19	Bi-directional
6	IO_TOP3	20	Bi-directional
7	IO_TOP4	21	Bi-directional
8	IO_TOP5	23	Bi-directional
9	IO_TOP6	41	Bi-directional
10	IO_TOP7	42	Bi-directional
11	IO_TOP8	43	Bi-directional
12	IO_TOP9	44	Bi-directional
13	GND	-	Power
14	EXT_RESET_IN#	-	Reset/Input
15	IO_TOP10	45	Bi-directional
16	IO_TOP11	46	Bi-directional
17	IO_TOP12	47	Bi-directional
18	IO_TOP13	48	Bi-directional
19	IO_TOP14	49	Bi-directional
20	IO_TOP15	53	Bi-directional
21	IO_TOP16	54	Bi-directional
22	IO_TOP17	55	Bi-directional
23	IO_TOP18	56	Bi-directional
24	IO_TOP19	57	Bi-directional
25	IO_TOP20	58	Bi-directional
26	IO_TOP21	59	Bi-directional
27	GND	-	Power
28	INT_CLOCK_B_TOP	-	Clock/Output
29	IO_TOP22	60	Bi-directional
30	IO_TOP23	61	Bi-directional
31	IO_TOP24	62	Bi-directional
32	IO_TOP25	63	Bi-directional
33	IO_TOP26	64	Bi-directional
34	IO_TOP27	65	Bi-directional
35	IO_TOP28	66	Bi-directional
36	IO_TOP29	67	Bi-directional
37	IO_TOP30	68	Bi-directional
38	IO_TOP31	74	Bi-directional
39	VCC_IO_EXT	-	Power/Input
40	GND	-	Power

J2 (Top side)			
Header Pin #	Signal Name	FPGA Pin #	Signal Type
1	VCC_EXT	-	Power/Input
2	GND	-	Power
3	IO_TOP32	240	Bi-directional
4	IO_TOP33	239	Bi-directional
5	IO_TOP34	238	Bi-directional
6	IO_TOP35	237	Bi-directional
7	IO_TOP36	236	Bi-directional
8	IO_TOP37	235	Bi-directional
9	IO_TOP38	234	Bi-directional
10	IO_TOP39	233	Bi-directional
11	IO_TOP40	227	Bi-directional
12	IO_TOP41	226	Bi-directional
13	GND	-	Power
14	CLOCK_IN_TOP	-	Clock/Input
15	IO_TOP42	225	Bi-directional
16	IO_TOP43	224	Bi-directional
17	IO_TOP44	223	Bi-directional
18	IO_TOP45	222	Bi-directional
19	IO_TOP46	219	Bi-directional
20	IO_TOP47	218	Bi-directional
21	IO_TOP48	217	Bi-directional
22	IO_TOP49	216	Bi-directional
23	IO_TOP50	215	Bi-directional
24	IO_TOP51	214	Bi-directional
25	IO_TOP52	213	Bi-directional
26	IO_TOP53	208	Bi-directional
27	GND	-	Power
28	PLL1_OUT_TOP	-	Clock/Output
29	IO_TOP54	207	Bi-directional
30	IO_TOP55	206	Bi-directional
31	IO_TOP56	203	Bi-directional
32	IO_TOP57	202	Bi-directional
33	IO_TOP58	201	Bi-directional
34	IO_TOP59	200	Bi-directional
35	IO_TOP60	197	Bi-directional
36	IO_TOP61	196	Bi-directional
37	IO_TOP62	195	Bi-directional
38	IO_TOP63	194	Bi-directional
39	INT_CLOCK_A_TOP	-	Clock/Output
40	GND	-	Power